

PATENT
81788.002512/ Drawing
P. Walter
4-2-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Art Unit: 2814

Seiichi Mori

Examiner: WEISS, H.

Serial No.: 09/451,619

Filed: November 30, 1999

For: NON-VOLATILE SEMICONDUCTOR
MEMORY HAVING A DECREASED GATE
LENGTH AND MANUFACTURING
METHOD THEREOF (As Amended)**LETTER TO THE OFFICIAL DRAFTSPERSON**Assistant Commissioner for Patents
Washington, D.C. 20231
Attention: AmendmentI hereby certify that this correspondence is
being facsimile transmitted to be received in
the United States Patent and Trademark
Office, Fax No. 703-308-7722 on March 21,
2002.Sterlon R. Mason Reg. No. 41,179
Name
Signature Date
March 22, 2002

Dear Sir:

Applicant respectfully requests approval of two (2) sheets of proposed drawings changes with changes to Figures 1 and 5 shown in red ink. Applicant respectfully submits proposed changes to Figures 1 and 5 changing reference character "7" to reference character "8" for the source region in Figure 1 and incorporating the legend "Prior Art" in Figure 5. No new matter has been added.

Respectfully submitted,
HOGAN & HARTSON, L.L.P.

Date: March 22, 2002

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